


**Module handbook**  
**for the**  
**Master of Information and Electrical Engineering**  
**of the University of Wismar**  
**University of Applied Sciences: Technology, Business and Design**

07.01.2014

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## Module 01: Project Seminar

<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Project Seminar</i>
<b>Abbreviation</b>	<i>PS</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>0/2/2/0</i>
<b>Semester:</b>	<i>Annually in the summer semester</i>
<b>Person responsible for the module:</b>	<i>Professor heading the course</i>
<b>Lecturer:</b>	<i>Professor heading the course</i>
<b>Language:</b>	<i>German or English</i>
<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering</i>
<b>Teaching format / contact hours (CH):</b>	<i>2 CH seminars, 2 CH tutorials/project work Approved number of participants: Seminars 35, tutorials 20, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	
<b>Learning targets / competencies:</b>	<i>Ability to work independently on typical technical tasks in electrical engineering</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Practical tasks in the particular specialist area are worked on separately in project groups</i></li> <li>• <i>Project progress is discussed between the project groups under the guidance of the university teaching staff</i></li> </ul>
<b>Assessment types:</b>	<i>Alternative assessment, see Appendix 1 of the examination regulations Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i>
<b>Media formats:</b>	<i>Board presentation, PowerPoint presentation</i>
<b>Literature:</b>	 <i>Current literature adjusted to the issues and the specialist area</i>









## Module 02: Simulation of Complex Systems


<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Simulation of Complex Systems</i>
<b>Abbreviation, if applicable</b>	<i>SKS</i>
<b>Subtitle, if applicable</b>	
<b>Teaching sessions, if applicable:</b>	<i>1/1/0/2</i>

<b>(L / S / T / P)</b>	
<b>Semester:</b>	<i>Annually in the summer semester</i>
<b>Person responsible for the module:</b>	<i>Prof. S. Pawletta</i>
<b>Lecturer:</b>	<i>Prof. S. Pawletta / Prof. Auer</i>
<b>Language:</b>	<i>German or English</i>
<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering</i>
<b>Teaching format / contact hours (CH):</b>	<i>1 CH lecture, 1 CH seminar, 2 CH laboratory-based practical classes Approved number of participants: Lectures 60, seminars 35, practical classes 15, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Basic proficiency in handling SCEs (Matlab, etc.)</i>
<b>Learning targets / competencies:</b>	<i>Ability to model, simulate and analyse complex discrete event and hybrid systems</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Modelling and simulation of discrete event and hybrid systems</i></li> <li>• <i>Practical examples of application using SCEs (Matlab, etc.)</i></li> </ul>
<b>Assessment types:</b>	<i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i>
<b>Media formats:</b>	<i>Board presentation, overhead presentation, scripts and websites presented in lectures</i>
<b>Literature:</b>	 <i>Abel, D.; Bollig, A.: Rapid Control Prototyping – Methoden und Anwendungen, Springer Verlag</i>


### Module 03: Quality Management

<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Quality Management</i>
<b>Abbreviation</b>	<i>QM</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>1/1/2/0</i>
<b>Semester:</b>	<i>Annually in the winter semester</i>
<b>Person responsible for the module:</b>	<i>Prof. M. Krüger</i>
<b>Lecturer:</b>	<i>Prof. M. Krüger</i>
<b>Language:</b>	<i>German or English</i>

<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering</i>
<b>Teaching format / contact hours (CH):</b>	<i>1 CH lecture, 1 CH seminar, 2 CH tutorials Approved number of participants: Lectures 60, seminars 35, tutorials 20, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study Workload for independent study: 1 CH</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Mathematics (stochastics)</i>
<b>Learning targets / competencies:</b>	<ul style="list-style-type: none"> <li>• <i>Knowledge of the fundamental relationships in QPM</i></li> <li>• <i>Ability to think and behave in a process-oriented manner</i></li> <li>• <i>Knowledge of elementary tools and methods for quality assurance as well as the ability to apply them based on targets</i></li> <li>• <i>Knowledge of quality planning, testing, control</i></li> <li>• <i>Knowledge of quality management systems and their demonstration and auditing/certification</i></li> </ul>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Introduction (terms, history, significance)</i></li> <li>• <i>Processes</i></li> <li>• <i>Creativity and visualisation techniques</i></li> <li>• <i>Tools and methods in QM (APQP, QFD, DoE, BSC ...)</i></li> <li>• <i>Total Quality Management</i></li> <li>• <i>Six Sigma</i></li> <li>• <i>EFQM Excellence Model</i></li> <li>• <i>Quality management systems</i></li> <li>• <i>Auditing and certification</i></li> <li>• <i>Quality awards</i></li> </ul>
<b>Assessment types:</b>	<i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i>
<b>Media formats:</b>	<i>Board presentation, PowerPoint presentation, scripts presented in lectures</i>
<b>Literature:</b>	<ul style="list-style-type: none"> <li> <i>Kamiske, Gerd F.; Brauer, Joerg-Peter: Qualitätsmanagement von A bis Z: Erläuterungen moderner Begriffe des Qualitätsmanagements .- München [u.a.] : Hanser, 2006</i></li> <li> <i>Hering, Ekbert (Hrsg.): Qualitätsmanagement für Ingenieure .- Berlin [u.a.]: Springer, 2003</i></li> <li> <i>Qualität und Zuverlässigkeit: Qualitätsmanagement in Industrie und Dienstleistung, Organ der DGQ, Hansa Verlag</i></li> <li> <i>Magnusson, Kjell [u. a.]: Six Sigma umsetzen: Die neue Qualitätsstrategie für Unternehmen .- München, Wien: Hansa, 2001</i></li> <li> <i>Ziege, Kathrin: Erstellung und Einführung eines Qualitätsmanagementsystems . - Bremen : Europäischer Hochschulverl., 2009</i></li> <li> <i>Pfeifer, Tilo: Qualitätsmanagement : Strategien, Methoden, Techniken . - München : Hanser, Carl, 2008</i></li> <li> <i>Klein, Bernd: Versuchsplanung - DoE : Einführung in die Taguchi/Shainin-Methodik . - München [u.a.] : Oldenbourg, 2007</i></li> <li> <i>Masing, Walter: Handbuch Qualitätsmanagement . -</i></li> </ul>










	<p>München : Hanser, 2007</p> <p> Gertz, Stefanie: <i>Der schnelle und einfache Weg zu Business-Excellence mit Hilfe des EFQM-Modells</i> . - Kissing : WEKA Media, 2005-</p>
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## Module 04: Research Seminar

<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Research Seminar</i>
<b>Abbreviation</b>	<i>FoS</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>0/1/3/0</i>
<b>Semester:</b>	<i>Annually in the winter semester</i>
<b>Person responsible for the module:</b>	<i>Professor heading the course</i>
<b>Lecturer:</b>	<i>Professor heading the course</i>
<b>Language:</b>	<i>German or English</i>
<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering</i>
<b>Teaching format / contact hours (CH):</b>	<i>1 CH seminar, 3 CH tutorials Approved number of participants: Seminars 35, tutorials 20, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study Workload for independent study: 1 CH</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	
<b>Learning targets / competencies:</b>	<i>Ability to work independently on typical technical tasks with a research character</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Practical tasks in the particular specialist area are worked on separately in project groups</i></li> <li>• <i>Project progress is discussed between the project groups under the guidance of the university teaching staff</i></li> </ul>
<b>Assessment types:</b>	<i>Alternative assessment, see Appendix 1 of the examination regulations Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i>
<b>Media formats:</b>	<i>Board presentation, PowerPoint presentation</i>
<b>Literature:</b>	<p> <i>Current literature adjusted to the issues and the specialist area</i></p>








## Module 05: Microsystems Engineering II

<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Microsystems Engineering II</i>
<b>Abbreviation</b>	<i>MiSyT II</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>1/1/1/1</i>
<b>Semester:</b>	<i>Annually in the summer semester</i>
<b>Person responsible for the module:</b>	<i>Prof. Wienecke</i>
<b>Lecturer:</b>	<i>Prof. Wienecke / Dr Barfels</i>
<b>Language:</b>	<i>German or English</i>
<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering, specialist area information and communication engineering; Elective in the specialist areas automation and mechatronics and electrical power engineering</i>
<b>Teaching format / contact hours (CH):</b>	<i>1 CH lecture, 1 CH seminar, 1 CH tutorial, 1 CH laboratory practical class Approved number of participants: Lectures 60, seminars 35, tutorials 20, practical classes 15, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Basic knowledge of materials and technologies in electrical engineering, microsystems technology</i>
<b>Learning targets / competencies:</b>	<i>With the aim of miniaturising components and processes, the methods and technologies in microelectronics are increasingly applied across broad areas of industry, particularly in the areas of medicine, environment and sensors. In this compulsory module students are taught to assess and apply the principles of operation, use and manufacturing methods of such sensor/actuator systems.</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Basic technologies in microsystems technology</i></li> <li>• <i>New materials in medical, environmental and sensor technology, sensor properties</i></li> <li>• <i>Specific measurement technologies</i></li> <li>• <i>From macrotechnology to nanotechnology, examples of applications and production methods</i></li> <li>• <i>Production methods for electrical and optochemical sensors, biosensors</i></li> <li>• <i>Sensor/actuator systems, examples of applications</i></li> <li>• <i>Projects: e.g. optically switched hydrogen sensor</i></li> </ul>
<b>Assessment types:</b>	<i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i>
<b>Media formats:</b>	<i>Board presentation, PowerPoint presentation, overhead presentation, scripts presented in lectures, project-based teaching</i>

	<i>units</i>
<b>Literature:</b>	 W. Menz, J. Mohr, O. Paul: <i>Microsystem Technology</i> ,  Wiley-VCH, Weinheim, NY, 2001  J. Frühauf: <i>Werkstoffe der Mikrotechnik</i> ,  Fachbuchverlag Leipzig, 2005  M. Köhler: <i>Nanotechnologie</i> ,  Wiley-VCH, Weinheim, NY, 2001  W. Göpel, J. Hesse, J. N. Zemel, (Hrsg.): <i>Sensors</i> ,  Wiley-VCH, Weinheim, NY, 1991  Work with literature and patent databases (e.g. <i>INSPEC, ESPACENET</i> )

## Module 06: Communication Systems








<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Communication Systems</i>
<b>Abbreviation</b>	<i>CoSy</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>1/1/1/1</i>
<b>Semester:</b>	<i>Annually in the summer semester</i>
<b>Person responsible for the module:</b>	<i>Prof. Ahrens</i>
<b>Lecturer:</b>	<i>Prof. Ahrens</i>
<b>Language:</b>	<i>German or English</i>
<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering, specialist area information and communication engineering</i>
<b>Teaching format / contact hours (CH):</b>	<i>1 CH lecture, 1 CH seminar, 1 CH tutorial, 1 CH laboratory practical class Approved number of participants: Lectures 60, seminars 35, tutorials 20, practical classes 15, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Fundamentals of Communications</i>
<b>Learning targets / competencies:</b>	<i>Getting familiar with basic concepts and algorithms for digital data transmission over dispersive channels</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Introduction to digital communications</i></li> <li>• <i>Spread-Spectrum Systems</i></li> <li>• <i>Multicarrier Transmission</i></li> <li>• <i>MIMO Systems</i></li> </ul>
<b>Assessment types:</b>	<i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i>
<b>Media formats:</b>	<i>Board presentation and PowerPoint presentation, scripts presented in lectures in the form of work sheets</i>

<b>Literature:</b>	 Haykin, S.; Moher, M.: <i>Communication Systems</i> . Chichester: Wiley, 2010  Ziemer, R.E.; Tranter, W. H.: <i>Principles of Communications: Systems, Modulation and Noise</i> . Chichester: Wiley, 2010  Goldsmith, A.: <i>Wireless Communications</i> . New York Cambridge, 2005  Öberg, T.: <i>Modulation, Detection and Coding</i> . Chichester: Wiley, 2001  Bahai, A.R.S.; Saltzberg, B.R. Ergen, M.: <i>Multi-Carrier Digital Communications - Theory and Applications of OFDM</i> . New York: Springer, 2004  Kühn, V.: <i>Wireless Communications over MIMO Channels - Applications to CDMA and Multiple Antenna Systems</i> , Chichester: Wiley, 2006  Proakis, J. G.: <i>Digital communications</i> . Boston: McGraw-Hill, 2000
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## Module 07: Network and Security Management

<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Network and Security Management</i>
<b>Abbreviation</b>	<i>NWSM</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>1/1/0/2</i>
<b>Semester:</b>	<i>Annually in the summer semester</i>
<b>Person responsible for the module:</b>	<i>Prof. Jonas</i>
<b>Lecturer:</b>	<i>Prof. Jonas</i>
<b>Language:</b>	<i>English</i>
<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering, specialist area information and communication engineering</i>
<b>Teaching format / contact hours (CH):</b>	<i>1 CH lecture, 1 CH seminar, 2 CH laboratory-based practical classes Approved number of participants: Lectures 60, seminars 35, practical classes 15, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 15 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Operating Systems, System and Network Programming,</i>
<b>Learning targets / competencies:</b>	<i>Competencies for planning, design and management of small computer networks, Competencies for evaluation of security mechanisms, design and implementation of security components, development of security policies</i>



<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>FCAPS: fault, configuration, accounting, performance, security management</i></li> <li>• <i>OSI Management and Internet Management (SNMP)</i></li> <li>• <i>WEB-based management architectures</i></li> <li>• <i>Management tools, network monitoring</i></li> <li>• <i>Identity management, policies, management of firewalls and proxies</i></li> <li>• <i>Implementation of network security</i></li> </ul>
<b>Assessment types:</b>	<p><i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations</i></p> <p><i>Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i></p>
<b>Media formats:</b>	<i>Board presentation, PowerPoint presentation, overhead presentation, scripts presented in lectures</i>
<b>Literature:</b>	<ul style="list-style-type: none"> <li> <i>Tipton H. F., Krause, M.: Information Security Management Handbook, Auerbach Publishers Inc. 2003</i></li> <li> <i>McNab Chris: Network Security Assessment, O'Reilly 2009</i></li> <li> <i>Rose, M. T.: A Simple Book – An Introduction to Management of TCP/IP based Internets. Prentice Hall 1994</i></li> <li> <i>Sloman, M.: Network and Distributed Systems Management. Addison Wesley 1994</i></li> <li> <i>stallings, W.: SNMP, SNMPv2, SNMPv3 and RMON 1 and 2, Addison Wesley 1999</i></li> <li> <i>Subramanian, M.: Network Management – Principles and Practice. Addison Wesley 2000</i></li> <li> <i>Zwicky, Cooper, Chapman: Building Internet Firewalls. O'Reilly &amp; Associates 2000</i></li> </ul>

## Module 08: Advanced Topics in Communications

<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Advanced Topics in Communications</i>
<b>Abbreviation</b>	<i>ATC</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>1/1/2/0</i>
<b>Semester:</b>	<i>Annually in the winter semester</i>
<b>Person responsible for the module:</b>	<i>Prof. Lochmann</i>
<b>Lecturer:</b>	<i>Prof. Lochmann and Prof. Ahrens</i>
<b>Language:</b>	<i>German or English</i>
<b>Classification in the curriculum</b>	<p><i>Compulsory module in the Master of Information and Electrical Engineering, specialist area information and communication engineering</i></p> <p><i>Elective in the specialist areas automation and mechatronics and electrical power engineering</i></p>
<b>Teaching format / contact hours (CH):</b>	<p><i>1 CH lecture, 1 CH seminar, 2 CH tutorials</i></p> <p><i>Approved number of participants:</i></p>

	<i>Lectures 60, seminars 35, tutorials 20, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Fundamentals of Communications, Communication Systems</i>
<b>Learning targets / competencies:</b>	<i>Getting familiar with advanced concepts and algorithms for digital data transmission over dispersive channels</i>
<b>Contents:</b>	<i>The course covers selected topics of advanced signal processing schemes and developments. Selected problems are solved with Matlab in small groups during the exercises.</i>
<b>Assessment types:</b>	<i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i>
<b>Media formats:</b>	<i>Board presentation and PowerPoint presentation, scripts presented in lectures in the form of work sheets</i>
<b>Literature:</b>	 Goldsmith, A.: <i>Wireless Communications</i> . New York: Cambridge, 2005  Öberg, T.: <i>Modulation, Detection and Coding</i> . Chichester: Wiley, 2001  Haykin, S.; Moher, M.: <i>Communication Systems</i> . Chichester: Wiley, 2010  Ziemer, R.E.; Tranter, W. H.: <i>Principles of Communications: Systems, Modulation and Noise</i> . Chichester: Wiley, 2010  Kühn, V.: <i>Wireless Communications over MIMO Channels - Applications to CDMA and Multiple Antenna Systems</i> , Wiley, Chichester, 2006.  Proakis, J. G.: <i>Digital communications</i> . Boston: McGraw-Hill, 2000  Eberlein, D.: <i>Lichtwellenleiter-Technik: Grundlagen, Verbindungs- und Messtechnik, Systeme, Trends</i> . Expert-Verlag, Renningen 2002  Kauffels, F.: <i>Optische Netze</i> . mitp-Verlag, Bonn 2002  Krauss, O.: <i>DWDM und optische Netze: Eine Einführung in die Terabit-Technologie</i> . Publicis Corp. Publ. Erlangen 2002

## Module 09: Advanced Optical Communications





<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Advanced Optical Communications</i>
<b>Abbreviation</b>	<i>AOC</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>1/1/1/1</i>
<b>Semester:</b>	<i>Annually in the winter semester</i>

<b>Person responsible for the module:</b>	<i>Prof. Lochmann</i>
<b>Lecturer:</b>	<i>Prof. Lochmann</i>
<b>Language:</b>	<i>German or English</i>
<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering, specialist area information and communication engineering</i>
<b>Teaching format / contact hours (CH):</b>	<i>1 CH lecture, 1 CH seminar, 1 CH tutorial, 1 CH laboratory practical class Approved number of participants: Lectures 60, seminars 35, tutorials 20, practical classes 15, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Knowledge of the principles of optical communications</i>
<b>Learning targets / competencies:</b>	<i>Ability to describe mathematically optical sign propagation and modification in components and systems</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Calculation of mode fields</i></li> <li>• <i>Beam propagation method (BPM)</i></li> <li>• <i>Photonic lightwave circuits</i></li> <li>• <i>Nonlinear behaviour of lightwave circuits</i></li> <li>• <i>Optical amplifier</i></li> <li>• <i>Modulation methods</i></li> <li>• <i>Detection principles, SNR analysis</i></li> <li>• <i>Analysis and calculation of lightwave circuit systems</i></li> </ul>
<b>Assessment types:</b>	<i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i>
<b>Media formats:</b>	<i>Board presentation and PowerPoint presentation, scripts presented in lectures in the form of work sheets</i>
<b>Literature:</b>	 <i>Eberlein, D.: Lichtwellenleiter-Technik: Grundlagen, Verbindungs- und Messtechnik, Systeme, Trends. Expert-Verlag, Renningen 2002</i>  <i>Kauffels, F.: Optische Netze. mitp-Verlag, Bonn 2002</i>  <i>Krauss, O.: DWDM und optische Netze: Eine Einführung in die Terabit-Technologie. Publicis Corp. Publ. Erlangen 2002</i>  <i>Brückner, V.: Optische Nachrichtentechnik. Teubner-Verlag Leipzig 2003</i>

## Module 10: Integrated Circuit Design




<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Integrated Circuit Design</i>
<b>Abbreviation</b>	<i>SKE</i>
<b>Subtitle</b>	

<b>Teaching sessions, if applicable: (L / S / T / P)</b>	1/1/0/2
<b>Semester:</b>	Annually in the winter semester
<b>Person responsible for the module:</b>	Prof. Müller
<b>Lecturer:</b>	Prof. Müller
<b>Language:</b>	German or English
<b>Classification in the curriculum</b>	Compulsory module in the Master of Information and Electrical Engineering, in the specialist areas information and communication engineering and automation and mechatronics; elective in the specialist area electrical power engineering
<b>Teaching format / contact hours (CH):</b>	1 CH lecture, 1 CH seminar, 2 CH laboratory-based practical classes Approved number of participants: Lectures 60, seminars 35, practical classes 15, complies with capacity guidelines
<b>Workload:</b>	150 h including 16 weeks each of 4 CH on-campus study
<b>Credits:</b>	5 CR
<b>Entry requirements:</b>	Knowledge of digital circuit design, programming
<b>Learning targets / competencies:</b>	Ability to design complex digital circuits in VHDL and implement complex circuits in FPGAs
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• Architectures of programmable logic circuits</li> <li>• Circuit design with hardware description languages</li> <li>• Programming in VHDL</li> <li>• Simulation and implementation of complex digital circuits</li> <li>• Laboratory practical class</li> </ul>
<b>Assessment types:</b>	120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)
<b>Media formats:</b>	Board presentation, PowerPoint presentation, overhead presentation, scripts presented in lectures
<b>Literature:</b>	<p> Wannemacher, M.: Das FPGA – Kochbuch. 1. Auflage, Bonn, Internat. Thomson Publ., 1998</p> <p> Sikora, A.: Programmierbare Logikbausteine. Hanser – Verlag 2001</p> <p> Auer, A.: Programmierbare Logic – IC. 2. Auflage, Hüthig – Verlag Heidelberg 1994</p> <p> Auer, A.; Rudolf, D.: FPGA.Hüthig – Verlag Heidelberg 1995</p> <p> Herrmann, G.; Müller, D.: ASIC – Entwurf und Test. Fachbuchverlag Leipzig 2004</p> <p> Reifschneider, N.: CAE-gestützte IC-Entwurfsmethoden. Prentice Hall</p> <p> Mäder, A.: VHDL Kompakt.</p> <p> Ritter, J.; Molitor, P.: VHDL eine Einführung. Pearson 2004</p> <p> Jorke, G.: Rechnergestützter Entwurf digitaler Schaltungen., Hanser - Verlag 2004</p>

	 <i>Reichardt, J.; Schwarz, B.: VHDL-Synthese. Oldenbourg Verlag 2003</i>  <i>Hervé, Y.: VHDL-AMS. Oldenbourg Verlag 2006</i>  <i>Siemers, Ch.: Prozessorbau. Hanser Verlag Verlag 1999</i>  <i>Kesel, F; Bartholomä, R.: Entwurf von digitalen Schaltungen und Systemen mit HDLs und FPGAs. Oldenbourg Verlag 2006</i>
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## Module 11: Advanced Control II

<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Control Engineering II</i>
<b>Abbreviation</b>	<i>ReTe II</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>1/1/0/2</i>
<b>Semester:</b>	<i>Annually in the summer semester</i>
<b>Person responsible for the module:</b>	<i>Prof. Dünow</i>
<b>Lecturer:</b>	<i>Prof. Dünow</i>
<b>Language:</b>	<i>German or English</i>
<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering, specialist area automation and mechatronics</i>
<b>Teaching format / contact hours (CH):</b>	<i>1 CH lecture, 1 CH seminar, 2 CH laboratory-based practical classes</i> <i>Approved number of participants:</i> <i>Lectures 60, seminars 35, practical classes 15, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Mathematics, automation engineering, principles of control engineering</i>
<b>Learning targets / competencies:</b>	<i>Ability to design complex control systems using models</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Multivariable systems</i></li> <li>• <i>Modelling</i></li> <li>• <i>State space methods</i></li> <li>• <i>Robust controls</i></li> <li>• <i>Computer-aided design</i></li> <li>• <i>Advanced methods in control engineering</i></li> <li>• <i>(Choice)</i></li> </ul>
<b>Assessment types:</b>	<i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations</i> <i>Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i>

<b>Media formats:</b>	<i>Board presentation, PowerPoint presentation, board presentation, experiment presentation, simulation, scripts</i>
<b>Literature:</b>	 <i>Graham C. Goodwin; Stefan F. Graebe; Mario E. Salgado. Control System Design. Pearson US Imports, PHIPEs, 2000.</i>  <i>J. Lunze. Regelungstechnik Band II, Systemtheoretische Grundlagen, Analyse und Entwurf Einschleifiger Regelungen. Springer-Verlag, 2001.</i>  <i>H. Unbehauen. Regelungstechnik Band I bis III. Vieweg-Verlag, 2001.</i>





## Module 12: Embedded Control Systems II

<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Embedded Control Systems II</i>
<b>Abbreviation</b>	<i>ECSy II</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>1/1/0/2</i>
<b>Semester:</b>	<i>Annually in the summer semester</i>
<b>Person responsible for the module:</b>	<i>Prof. Dr. Simanski</i>
<b>Lecturer:</b>	<i>Prof. Dr. Simanski</i>
<b>Language:</b>	<i>German or English</i>
<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering, specialist area automation and mechatronics</i>
<b>Teaching format / contact hours (CH):</b>	<i>1 CH lecture, 1 CH seminar, 2 CH laboratory-based practical classes Approved number of participants: Lectures 60, seminars 35, practical classes 15, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Physics</i>
<b>Learning targets / competencies:</b>	<i>Ability to design distributed controls based on embedded systems, expertise in evaluating and selecting systems</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Model-based control design</i></li> <li>• <i>Self-configuring systems</i></li> <li>• <i>Diagnostic methods using models</i></li> <li>• <i>Real-time communication in distributed embedded systems</i></li> <li>• <i>Device design based on embedded systems</i></li> <li>• <i>Design tools</i></li> <li>• <i>Selected applications</i></li> </ul>
<b>Assessment types:</b>	<i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations</i>

	<i>Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i>
<b>Media formats:</b>	<i>Board presentation, PowerPoint presentation, board presentation, experiment presentation, simulation, scripts</i>
<b>Literature:</b>	<ul style="list-style-type: none"> <li> <i>Peter Marwedel, "Embedded System Design", Springer, Berlin; 2nd Print (1. November 2005), ISBN-10: 0387292373.</i></li> <li> <i>H. Kopetz, "Real-Time Systems, Design Principles for Distributed Embedded Applications", Kluwer Academic Publishers, Boston, Dordrecht, London, 1997.</i></li> <li> <i>D.D. Gajski, F. Vahid, S. Narayan, J. Gong, "Specification and Design of Embedded Systems", Prentice Hall, 1994.</i></li> </ul>

### Module 13: Sensor Systems/Actuators







<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Sensors/Actuators</i>
<b>Abbreviation</b>	<i>S/A</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>1/1/0/2</i>
<b>Semester:</b>	<i>Annually in the summer semester</i>
<b>Person responsible for the module:</b>	<i>Prof. Dünow</i>
<b>Lecturer:</b>	<i>Prof. Dünow</i>
<b>Language:</b>	<i>German or English</i>
<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering, specialist area automation and mechatronics as well as electrical power engineering; Elective in the specialist area information and communication engineering</i>
<b>Teaching format / contact hours (CH):</b>	<i>1 CH lecture, 1 CH seminar, 2 CH laboratory-based practical classes Approved number of participants: Lectures 60, seminars 35, practical classes 15, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Physics</i>
<b>Learning targets / competencies:</b>	<i>Ability to apply and develop sensor systems, familiarity with diverse operating principles and their application</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Sensor terms, functional structures</i></li> <li>• <i>Measurement effects</i></li> <li>• <i>Sensor signal detection and processing</i></li> <li>• <i>Selected measurement techniques</i></li> <li>• <i>Multisensor systems</i></li> </ul>

	<ul style="list-style-type: none"> <li>• <i>Model-based information acquisition</i></li> <li>• <i>(Virtual sensors)</i></li> <li>• <i>Operating principles and application</i></li> </ul>
<b>Assessment types:</b>	<p><i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations</i></p> <p><i>Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i></p>
<b>Media formats:</b>	<i>Board presentation, PowerPoint presentation, board presentation, experiment presentation, simulation, scripts</i>
<b>Literature:</b>	 <i>Bonfig, K.W. Sensoren und Sensorsysteme. Expert-Verlag 1991</i>  <i>Hoffmann, J. Taschenbuch der Messtechnik, Fachbuchverl. Leipzig, 1998</i>  <i>Schrüfer, E., Elektrische Messtechnik. Hanser, 2004</i>  <i>Tränkler, H.R.; Sensortechnik. Oldenbourg, 1996 2000</i>

## Module 14: Selected Aspects in Automation




<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Selected Aspects in Automation</i>
<b>Abbreviation</b>	<i>AAAT</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>1/1/0/2</i>
<b>Semester:</b>	<i>Annually in the winter semester</i>
<b>Person responsible for the module:</b>	<i>NN</i>
<b>Lecturer:</b>	<i>NN</i>
<b>Language:</b>	<i>German or English</i>
<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering, specialist area automation and mechatronics; Elective in the specialist areas information and communication engineering and electrical power engineering</i>
<b>Teaching format / contact hours (CH):</b>	<i>1 CH lecture, 1 CH seminar, 2 CH laboratory practical classes</i> <i>Approved number of participants:</i> <i>Lectures 60, seminars 35, practical classes 15, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Control Engineering, Computational Engineering</i>
<b>Learning targets / competencies:</b>	<i>Ability to design distributed controls based on embedded systems, expertise in evaluating and selecting systems</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Fuzzy systems and fuzzy control (structures and design, fuzzy controls)</i></li> <li>• <i>Neuronal networks (network structures, design and applications)</i></li> <li>• <i>Predictive regulation and control methods (principle,</i></li> </ul>



	<p>algorithms, applications)</p> <ul style="list-style-type: none"> <li>Selected applications of modern automation approaches</li> </ul>
<b>Assessment types:</b>	<p>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations</p> <p>Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</p>
<b>Media formats:</b>	<p>Board presentation, PowerPoint presentation, board presentation, experiment presentation, simulation, scripts</p>
<b>Literature:</b>	<p> Bothe, H.-H.: Fuzzy-Logic, Springer-Verlag, Berlin</p> <p> Kruse, Rudolf; Gebhardt, Jörg; Klawonn, Frank: Fuzzy-Systeme</p> <p> Nauck, Klawonn, Kruse, Neuronale Netze und Fuzzy-Systeme, Viewegverlag</p> <p> C. E. Garcia, D. M. Prett, M. Morari, „Model predictive control: theory and practice – a survey“,</p> <p> Automatica, No. 25, pp. 335-348, 1987</p> <p> Maciejowski, Predictive Control with Constraints, Prentice Hall 2002</p>





## Module 15: Building Automation

<b>Degree course:</b>	Master of Information and Electrical Engineering
<b>Module name:</b>	Building Automation
<b>Abbreviation</b>	GA
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	1/1/1/1
<b>Semester:</b>	Annually in the winter semester
<b>Person responsible for the module:</b>	Prof. Mundt
<b>Lecturer:</b>	Prof. Mundt
<b>Language:</b>	German or English
<b>Classification in the curriculum</b>	<p>Compulsory module in the Master of Information and Electrical Engineering, in the specialist areas electrical power engineering and automation and mechatronics;</p> <p>Elective in the specialist area information and communication engineering</p>
<b>Teaching format / contact hours (CH):</b>	<p>1 CH lecture, 1 CH seminar, 1 CH tutorial, 1 CH laboratory practical class</p> <p>Approved number of participants: Lectures 60, seminars 35, tutorials 20, practical classes 15, complies with capacity guidelines</p>
<b>Workload:</b>	150 h including 16 weeks each of 4 CH on-campus study
<b>Credits:</b>	5 CR
<b>Entry requirements:</b>	None
<b>Learning targets /</b>	Ability to use modern building BUS systems with the aim of long-

<b>competencies:</b>	<i>term energy conservation and sustainability considering individual consumption of various buildings</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Sensors, actuators in the building sector</i></li> <li>• <i>BUS systems (EIB/KNX, LCN, DALI)</i></li> <li>• <i>Structure, topology, technology, applications</i></li> <li>• <i>EIBnet/IP communication</i></li> <li>• <i>Smart home, comfort, savings</i></li> </ul>
<b>Assessment types:</b>	<p><i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations</i></p> <p><i>Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i></p>
<b>Media formats:</b>	<i>Board presentation, experiment presentation, PowerPoint presentation, scripts presented in lectures</i>
<b>Literature:</b>	 <i>Merz: Gebäudeautomation, Hanser, 2007</i>  <i>Sauter: EIB Installation Bus System, Publicis, 2001</i>  <i>KNX Handbook, ZVEI, 2006</i>




## Module 16: Energy Conversion

<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Energy Conversion</i>
<b>Abbreviation</b>	<i>EU</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>1/2/0/1</i>
<b>Semester:</b>	<i>Annually in the summer semester</i>
<b>Person responsible for the module:</b>	<i>Prof. Timm</i>
<b>Lecturer:</b>	<i>Prof. Timm</i>
<b>Language:</b>	<i>German</i>
<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering, specialist area electrical power engineering; Elective in the specialist areas information and communication engineering and electrical engineering</i>
<b>Teaching format / contact hours (CH):</b>	<i>1 CH lecture, 2 CH seminars, 1 CH practical class Approved number of participants: Lectures 60, seminars 35, practical classes 20, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>None</i>
<b>Learning targets / competencies:</b>	<i>Ability to identify the importance of the link between physical principles and engineering and scientific implementation</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Mechanics and heat</i></li> </ul>

	<ul style="list-style-type: none"> <li>• <i>Thermodynamics</i> - cycle processes, entropy, Joule - Thomson</li> <li>• <i>Heat and transport</i> - heat radiation, conduction</li> <li>• <i>Gases in machines and equipment</i></li> </ul>
<b>Assessment types:</b>	<p>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations</p> <p>Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</p>
<b>Media formats:</b>	Board presentation, PowerPoint presentation, overhead presentation
<b>Literature:</b>	<p> Stroppe, H.: <i>Physik</i> Fachbuchverlag Leipzig 1994</p> <p> Hering, E.; Martin, R.; Stohrer, M.: <i>Physik für Ingenieure</i> Springer – Verlag 1999</p> <p> Leute, U.: <i>Physik und ihre Anwendungen in technik und Umwelt</i> Hanser 2004</p> <p> Cerbe, G. ; Hoffmann, H.-J. : <i>Einführung in die Thermodynamik</i> Hanser 1999</p>







## Module 18: Grid Operation

<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Grid Operation</i>
<b>Abbreviation</b>	<i>NB</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>1/1/1/1</i>
<b>Semester:</b>	<i>Annually in the winter semester</i>
<b>Person responsible for the module:</b>	<i>Prof. Mundt</i>
<b>Lecturer:</b>	<i>Prof. Mundt</i>
<b>Language:</b>	<i>German</i>
<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering, specialist area electrical power engineering</i>
<b>Teaching format / contact hours (CH):</b>	<p><i>1 CH lecture, 1 CH seminar, 1 CH tutorial, 1 CH laboratory practical class</i></p> <p><i>Approved number of participants: Lectures 60, seminars 35, tutorials 20, practical classes 15, complies with capacity guidelines</i></p>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Principles of electrical power engineering, grid elements, supply structures</i>
<b>Learning targets /</b>	<i>Ability to make comprehensive assessments for safe operation of</i>

<b>competencies:</b>	<i>electrical grids with the aim of achieving a high level of availability of electrical energy.</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Neutral point connection in energy grids</i></li> <li>• <i>Design of grids to protect against short circuit effects</i></li> <li>• <i>Symmetrical and asymmetrical faults, electrical arcs</i></li> <li>• <i>Switching processes, design of circuits</i></li> <li>• <i>Stability, regulation</i></li> </ul>
<b>Assessment types:</b>	<i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations</i> <i>Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i>
<b>Media formats:</b>	<i>Board presentation, PowerPoint presentation, overhead presentation, scripts presented in lectures</i>
<b>Literature:</b>	 <i>Schlabbach: Kurzschlussstromberechnung, VDE Verlag, 2003</i>  <i>Schlabbach: Sternpunktbehandlung, VDE Verlag, 2002</i>  <i>Heuck: Elektrische Energieversorgung, Vieweg, 2007</i>



## Module 19: Power Electronics II

<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Power Electronics</i>
<b>Abbreviation</b>	<i>LE II</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>1/1/1/1</i>
<b>Semester:</b>	<i>Annually in the winter semester</i>
<b>Person responsible for the module:</b>	<i>Prof. Dr. Chiadò Caponet</i>
<b>Lecturer:</b>	<i>Prof. Dr. Chiadò Caponet</i>
<b>Language:</b>	<i>German or English</i>
<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering, specialist area electrical power engineering; Elective in the specialist areas information and communication engineering and automation and mechatronics</i>
<b>Teaching format / contact hours (CH):</b>	<i>1 CH lecture, 1 CH seminar, 1 CH tutorial, 1 CH laboratory practical class</i> <i>Approved number of participants: Lectures 60, seminars 35, tutorials 20, practical classes 15, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Power Electronics I</i>
<b>Learning targets / competencies:</b>	<i>Knowledge of basic principles of power semiconductor devices and circuits</i>

<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Properties of power semiconductors</i></li> <li>• <i>Protective circuits</i></li> <li>• <i>Rectifier circuits</i></li> <li>• <i>Converter circuits</i></li> </ul>
<b>Assessment types:</b>	<p><i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations</i></p> <p><i>Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i></p>
<b>Media formats:</b>	<i>Projector presentation, board presentation, scripts presented in lectures</i>
<b>Literature:</b>	<ul style="list-style-type: none"> <li> <i>Michel, M.: Leistungselektronik, Springer Verlag</i></li> <li> <i>Hagmann, Gert: Leistungselektronik : Grundlagen und Anwendungen, Aula Verlag</i></li> <li> <i>Specovius, J.: Grundkurs Leistungselektronik, Vieweg-Teubner Verlag</i></li> <li> <i>Brosch, P. F.: Leistungselektronik : kompakte Grundlagen und Anwendungen</i></li> <li> <i>Jäger, Rainer: Leistungselektronik : Grundlagen und Anwendungen, VDE Verlag</i></li> <li> <i>Lappe, R.; Conrad, H.; Kronberg, M.: Leistungselektronik, Verlag Technik</i></li> </ul>








## Module 20: Parallel and Distributed Systems

<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Parallel and Distributed Systems</i>
<b>Abbreviation</b>	<i>PvSy</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (L / S / T / P)</b>	<i>1/1/0/2</i>
<b>Semester:</b>	<i>Annually in the winter semester</i>
<b>Person responsible for the module:</b>	<i>Prof. S. Pawletta</i>
<b>Lecturer:</b>	<i>Prof. S. Pawletta</i>
<b>Language:</b>	<i>German</i>
<b>Classification in the curriculum</b>	<i>Elective Module in the Master of Information and Electrical Engineering</i>
<b>Teaching format / contact hours (CH):</b>	<p><i>1 CH lecture, 1 CH seminar, 2 CH laboratory practical classes</i></p> <p><i>Approved number of participants: Lectures 60, seminars 35, practical classes 15, complies with capacity guidelines</i></p>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Basic principles of C and Matlab programming</i>
<b>Learning targets / competencies:</b>	<i>Ability to create parallel and distributed software applications</i>

<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Principles of parallel and distributed systems (hardware, software, paradigms)</i></li> <li>• <i>Examples of technical applications and projects in engineering</i></li> </ul>
<b>Assessment types:</b>	<p><i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations</i></p> <p><i>Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i></p>
<b>Media formats:</b>	<i>Board presentation, overhead presentation, scripts and websites presented in lectures</i>
<b>Literature:</b>	 <i>Culler, D. E. et al: Parallel Computer Architecture: A Hardware/Software Approach, Morgan Kaufmann</i>  <i>Fink, R.: Parallelverarbeitung mit wissenschaftlich-technischen Berechnungsumgebungen</i>


## Module 21: Microprocessor Engineering in Mobile Devices

<b>Degree course:</b>	<p><i>Master of Information and Electrical Engineering</i></p> <p><i>Master of Mechatronics</i></p> <p><i>Master of Multimedia Engineering</i></p>
<b>Module name:</b>	<i>Microprocessor Engineering in Mobile Devices</i>
<b>Abbreviation</b>	<i>MPmG</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable:</b>	
<b>Semester:</b>	<i>Annually in the summer semester</i>
<b>Person responsible for the module:</b>	<i>Prof. Buller</i>
<b>Lecturer:</b>	<i>Prof. Buller</i>
<b>Language:</b>	<i>German</i>
<b>Classification in the curriculum</b>	<p><i>Elective Module in the Master of Information and Electrical Engineering;</i></p> <p><i>Elective Module in the Master of Mechatronics;</i></p> <p><i>Elective Module in the Master of Multimedia Engineering</i></p>
<b>Teaching format / contact hours (CH):</b>	<p><i>1 CH lecture, 1 CH seminar, 2 CH practical classes</i></p> <p><i>Approved number of participants:</i></p> <p><i>Lectures 40, seminars 20, practical classes 8, complies with capacity guidelines</i></p>

<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Application-ready knowledge in the areas of microprocessor engineering, informatics, circuit engineering and programming</i>
<b>Learning targets / competencies:</b>	<i>Ability to develop concepts and technically detailed solutions for the use of microprocessors in mobile devices with direct user interfaces</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Summary and processor examples with 16/32 bit and 32 bit processing width (Blackfin Micro Signal Architecture, ARM(TM) – Cortex family)</i></li> <li>• <i>Power management</i></li> <li>• <i>CapSense(TM) and TrueTouch(TM) – menu control</i></li> <li>• <i>Technologies and control variants for graphic modules</i></li> <li>• <i>Location and movement detection, integrated sensors</i></li> <li>• <i>Interfaces for analogue and digital signals</i></li> <li>• <i>Programming and signal processing</i></li> <li>• <i>Examples of applications in the areas of biosignal and audio signal processing</i></li> </ul>
<b>Assessment types:</b>	<i>120 minute written examination</i>
<b>Media formats:</b>	<i>Board presentation, OpenOffice Impress and Mediator presentations, overhead presentation, scripts presented in lectures</i>
<b>Literature:</b>	<p> <i>Yiu, Joseph: The Definitive Guide to ARM® Cortex®-M3 and Cortex®-M4 Processors, Elsevier, 2013, ISBN-10: 0124080820</i></p> <p> <i>Gan, Woon-Seng; Sen M. Kuo: Micro Signal Architecture, Wiley, 2007, ISBN - 978-0-471-73841-1</i></p> <p> <i>Martin, Trevor: The Designer's Guide to the Cortex-M Processor Family: A Tutorial Approach, Elsevier, 2013; ISBN-10: 0080982964</i></p> <p> <i>Schwark, Stefan; Bernhard Wörndl-Aichriedler: Android Programmierung und Hardware-Steuerung, Elektor, 2013, ISBN 978-3-89567-272-7</i></p> <p> <i>CrossCore Embedded Studio C/C++ Compiler and Library Manual for Blackfin Processors ; Analog Devices, Inc., 2013, Part Number 82-100116-01</i></p> <p> <i>PSoC® 5LowPower Architecture Technical Reference Manual; Cypress Inc., 2013, Document No. 001-78426 Rev. *C</i></p> <p> <b>Focus:</b> <i>Use of technical documentation and additional materials provided by companies for the modules or development systems used in the practical class, updated versions of each</i></p>

## Module 22: Telecommunications Engineering Project

<b>Degree course:</b>	<i>Master-Studiengang Informations- und Elektrotechnik</i>
<b>Module name:</b>	<i>Telecommunications Engineering Project</i>

<b>Abbreviation</b>	<i>NP</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (LV/SU/Ü/P)</b>	<i>0/0/4/0</i>
<b>Semester:</b>	<i>Annually in the summer semester</i>
<b>Person responsible for the module:</b>	<i>Professor heading the course</i>
<b>Lecturer:</b>	<i>Professor heading the course</i>
<b>Language:</b>	<i>German or English</i>
<b>Classification in the curriculum</b>	<i>Elective Module in the Master of Information and Electrical Engineering</i>
<b>Teaching format / contact hours (CH):</b>	<i>4 CH tutorial Approved number of participants: Tutorials 20, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CR</i>
<b>Entry requirements:</b>	<i>Digital Communications, Communication Systems</i>
<b>Learning targets / competencies:</b>	<i>Ability to independently carry out typical tasks of electrical engineering</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Project groups work on practical tasks of telecommunications</i></li> <li>• <i>Acquired results are prepared as scientific documentations</i></li> <li>• <i>Project progress is discussed between the groups under guidance of lecturers</i></li> </ul>
<b>Assessment types:</b>	<i>Alternative assessment, see Appendix 1 of the examination regulations</i>
<b>Media formats:</b>	<i>Board presentation, overhead presentation, group discussions</i>
<b>Literature:</b>	 <i>Up-to-date literature matching the specific telecommunications task</i>



## **Module 23: Thermal, Air Conditioning and Refrigeration Engineering**

*see modul handbook of Master course Energy and Resource Efficient Technologies*

## **Module 24: Video Processing**

*see module handbook of Master course Applied Informatics - Multimedia Engineering*

## **Module 25: Flow Machines**

*see modul handbook of Master course Energy and Resource Efficient Technologies*




## **Module 26: Efficient Energy Management**




*see modul handbook of Master course Energy and Resource Efficient Technologies*

## **Module 27: Knowledge based Systems**

*see Module handbook of Master course Business Informatics*



## Module 28: Electrical Drive Engineering II

<b>Degree course:</b>	<i>Master of Information and Electrical Engineering</i>
<b>Module name:</b>	<i>Electrical Drive Engineering II</i>
<b>Abbreviation</b>	<i>AnT II</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (LV/SU/Ü/P)</b>	<i>1/1/1/1</i>
<b>Semester:</b>	<i>Annually in the summer semester</i>
<b>Person responsible for the module:</b>	<i>Prof. Dr.-Ing. Chiadò Caponet</i>
<b>Lecturer:</b>	<i>Prof. Dr.-Ing. Chiadò Caponet</i>
<b>Language:</b>	<i>German or English</i>
<b>Classification in the curriculum</b>	<i>Compulsory module in the Master of Information and Electrical Engineering, specialist area electrical power engineering</i>
<b>Teaching format / contact hours (CH):</b>	<i>1 CH lecture, 1 CH seminar, 2 CH practical class Approved number of participants: Lectures 60, seminars 35, practical classes 15, complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CP</i>
<b>Entry requirements:</b>	<i>Electrical Drive Engineering I</i>
<b>Learning targets / competencies:</b>	<i>Competence in the design and use of electric drive systems Behavior of induction machines, in particular with power converter feeding Field-Oriented Control of Electrical Drives: concepts and applications</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Dynamic model and behavior of the DC machine</i></li> <li>• <i>Induction machine and brushless machine supplied by voltage source inverter (Six-Steps and PWM control)</i></li> <li>• <i>Induction machine and brushless machine with field-oriented control</i></li> </ul>
<b>Assessment types:</b>	<i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i>
<b>Media formats:</b>	<i>Board presentation, overhead presentation</i>
<b>Literature:</b>	 <i>Hagl, R.,: Elektrische Antriebstechnik, 2 Auflage, Hanser</i>  <i>Fuest, K, Döring, P.:Elektrische Maschinen und Antriebe -Lehr- und Arbeitsbuch für Gleich-, Wechsel- und Drehstrommaschinen sowie Elektronische Antriebstechnik“, Viewegs</i>  <i>Vogel, J, u.a.: Grundlagen der elektrischen Antriebstechnik mit Berechnungsbeispiel, Veb Verlag</i>

	 <i>Technik Berlin</i>  <i>Probst, U.:</i> <i>Servoantriebe in der Automatisierungstechnik, Vieweg+Teubner</i>  <i>Mohan N.:</i> <i>Advanced Electric Drives: Analysis, Control, and Modeling Using MATLAB/Simulink®</i> , John Wiley & Sons, Inc., 2014
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## Module 29: Electrical Power Engineering II

<b>Degree course:</b>	<i>Master-Studiengang Informations- und Elektrotechnik</i>
<b>Module name:</b>	<i>Electrical Power Engineering II</i>
<b>Abbreviation</b>	<i>EET II</i>
<b>Subtitle</b>	
<b>Teaching sessions, if applicable: (LV/SU/Ü/P)</b>	<i>1/1/1/1</i>
<b>Semester:</b>	<i>Annually in the summer semester</i>
<b>Person responsible for the module:</b>	<i>Prof. Dr.-Ing. Chiadò Caponet</i>
<b>Lecturer:</b>	<i>Prof. Dr.-Ing. Chiadò Caponet</i>
<b>Language:</b>	<i>Deutsch</i>
<b>Classification in the curriculum</b>	<i>Elective Module in the Master of Information and Electrical Engineering</i>
<b>Teaching format / contact hours (CH):</b>	<i>1 CH lecture, 1 CH seminar, 1 CH tutorial, 1 CH laboratory practical class</i> <i>Approved number of participants:</i> <i>Lectures 60, seminars 35, tutorials 20, practical classes 15,</i> <i>complies with capacity guidelines</i>
<b>Workload:</b>	<i>150 h including 16 weeks each of 4 CH on-campus study</i>
<b>Credits:</b>	<i>5 CP</i>
<b>Entry requirements:</b>	<i>Electric Power Engineering I</i>
<b>Learning targets / competencies:</b>	<i>Knowledge of photovoltaic systems: construction, operating principle and design</i> <i>Knowledge of wind turbines: construction, operating principle and design</i>
<b>Contents:</b>	<ul style="list-style-type: none"> <li>• <i>Operating principle of photovoltaic cells, comparison of different technologies, electrical parameters, equivalent circuit diagram. Integration in photovoltaic systems, island systems, network coupling.</i></li> <li>• <i>Construction and operating principle of modern wind turbines: fundamentals and concepts.</i></li> <li>• <i>Double-fed asynchronous wind generator. operating principle of the generator with wind strengths change and output voltage and frequency regulation. Synchronization to a running network. of Control of active and reactive power, frequency, voltage.</i></li> <li>• <i>Behavior of wind turbines in the case of grid faults</i></li> </ul>

<b>Assessment types:</b>	<i>120 minute written examination or 20 minute oral examination or alternative assessment, see Appendix 1 of the examination regulations Prerequisite for admission to examinations defined by the examination regulations, section 7 (4)</i>
<b>Media formats:</b>	<i>Board presentation, overhead presentation</i>
<b>Literature:</b>	 <i>Heuck K., Dettmann K, Schulz D., Elektrische Energieversorgung, 9., Auflage, SpringerVieweg Wiesbaden</i>  <i>Wagner A, Photovoltaik Engineering, Springer Verlag, Berlin, Heidelberg, New York</i>